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APPLICANTS Tatsuo Hiramatsu, Osaka, JAPAN; Hiroshi Nakajima, Kyoto, JAPAN; Makoto Okada, Osaka, JAPAN; Makoto Ozone, Osaka, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP04/09811 07/09/2004				
** FOREIGN APPLICATIONS ***** JAPAN 2003-339030 09/30/2003 JAPAN 2003-362216 10/22/2003				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 11/09/2006				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <input checked="" type="checkbox"/> Allowance <input checked="" type="checkbox"/> Examiner's Signature <input checked="" type="checkbox"/> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 30	TOTAL CLAIMS 21
INDEPENDENT CLAIMS 4				
ADDRESS 20277				
TITLE Processing device with reconfigurable circuit, integrated circuit device and processing method using these devices				
FILING FEE RECEIVED 1150	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	